

10/042,604

CofC

PATENT
Attorney Docket: 728-225



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hsu et al.

Patent No: 6,751,156 *B2*

Issued: June 15, 2004

Certificate

For: SEMICONDUCTOR MEMORY
SYSTEM HAVING DYNAMICALLY
DELAYED TIMING FOR HIGH-SPEED
DATA TRANSFERS

SEP 02 2004

of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR PTO MISTAKE UNDER 37 C.F.R. §1.322

Sir:

The patentees hereby request the issuance of a Certificate of Correction to correct the errors in the patent.

The exact location where the errors occurred in the patent is indicated on the attached Form PTO/SB/44. This form is being sent in duplicate with at least one copy being suitable for printing.

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

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Dated: August 30, 2004


Helene Harrison

SEP 07 2004

It is Applicants' belief that these errors originated at the U.S. Patent and Trademark Office, and accordingly, no fee is believed to be required. Please charge any fees that may become due to Deposit Account No. 04-1121. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Paul J. Farrell".

Paul J. Farrell
Registration No. 33,494
Attorney for Applicant/s

DILWORTH & BARRESE, LLP
333 Earle Ovington Boulevard
Uniondale, New York 11553
(516) 228-8484

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 6,751,156 B2

DATED : June 15, 2004

INVENTOR(S) : Hsu et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 12, line 18, "clack" should be --clock--

Claim 14, line 42, "die" should be --the--

**Claim 15, line 3, ".firs:" should be --first--
line 10, "clack" should be --clock--**

MAILING ADDRESS OF SENDER:

Paul J. Farrell, Esq., Dilworth & Barrese, LLP
333 Earle Ovington Blvd.
Uniondale, NY 11553

PATENT NO. 6,751,156 B2

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